

**EAST Search History****EAST Search History (Prior Art)**

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp          |
|-------|------|---|---|------------------|---------|---------------------|
| L1    | 9056 | 714/5,6,25,30,42,764,773.ccls.  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>13:43 |
| L2    | 7602 | 714/5,6,25,30,42,764,773.ccls. and memory   | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>13:44 |
| L3    | 8172 | 714/5,6,25,30,42,764,773.ccls. and (memory or storage)  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>13:44 |
| L4    | 2755 | 714/5,6,25,30,42,764,773.ccls. and (memory or storage) and ((error fault failure) with correct\$3)  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>13:45 |
| L5    | 727  | 714/5,6,25,30,42,764,773.ccls. and (memory or storage) and ((error fault failure) with correct\$3) and request and external                 | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>13:45 |
| L6    | 36   | 714/5,6,25,30,42,764,773.ccls. and (memory or storage) and ((error fault failure) with correct\$3) and request and external and solid-state | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>14:12 |
| L7    | 0    | "solid-state memory" and "error corrector" and (request with external)  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>14:42 |
| L8    | 63   | "solid-state memory" and "error correction" and (request with external)   | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>14:43 |
| L9    | 6144 | memory with "error signal"  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>14:52 |
| L10   | 1161 | memory with "error signal" and request  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2009/11/24<br>14:53 |

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| L11 | 0    | memory with "error signal" and request with "external correction" | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/11/24<br>14:53 |
| L12 | 1    | memory and request with "external correction"                     | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/11/24<br>14:53 |
| S1  | 1    | 10/805182   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>13:42 |
| S2  | 1026 | 714/5.ccls.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/02<br>10:34 |
| S3  | 38   | S2 AND syndrome   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>13:42 |
| S4  | 2    | "5922080".pn.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>13:57 |
| S5  | 1    | 2001-175542   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>13:43 |
| S6  | 0    | S2 AND independant\$2.ab.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>13:57 |
| S7  | 37   | S2 AND independent\$2.ab.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>14:53 |
| S8  | 110  | S2 AND ((separate\$2 OR independent\$2) WITH ("same"))            | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/27<br>14:54 |

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| S9  | 1      | "03425171.0"                                     | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2006/12/29<br>16:43 |
| S10 | 1      | 2002-79269                                       | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/01/02<br>09:10 |
| S11 | 240    | 714/764.ccls.                                    | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/01/05<br>09:06 |
| S12 | 42     | S11 AND hamming ADJ code                         | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/01/05<br>09:06 |
| S13 | 321    | "solid state drive"                              | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2007/09/02<br>10:31 |
| S14 | 87     | 714/5.ccls. and @pd> = "20061229"                | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/02<br>10:35 |
| S15 | 6      | 714/764.ccls. and<br>@pd> = "20070105"           | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2007/09/02<br>10:36 |
| S16 | 706212 | "integrated circuit"                             | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2007/09/02<br>10:37 |
| S17 | 14018  | "integrated circuit" and "714".clas.             | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2007/09/02<br>10:57 |
| S18 | 87     | "integrated circuit" with ECC and<br>"714".clas. | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2007/09/02<br>10:57 |

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| S19 | 115  | 714/5.ccls. and @pd> = "20070902"  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/05/16<br>10:55 |
| S20 | 21   | 714/764.ccls. and<br>@pd> = "20070902"                                       | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/05/19<br>10:12 |
| S21 | 96   | ("solid state" with (non-volatile adj<br>memory)) and (error adj correction) | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/05/19<br>13:52 |
| S22 | 164  | 714/773.ccls.  | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:21 |
| S23 | 2536 | 714/6.ccls.  | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:23 |
| S24 | 182  | 714/6.ccls. and "solid state"  | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:23 |
| S25 | 1100 | 714/30.ccls.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:26 |
| S26 | 31   | 714/30.ccls. and "solid state"   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:26 |
| S27 | 905  | 714/42.ccls.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:27 |
| S28 | 68   | 714/42.ccls. and "solid state"   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/05/19<br>14:27 |

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| S29 | 112    | 714/5.ccls. and @pd> = "20080519"      | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/12/03<br>10:31 |
| S30 | 194    | 714/6.ccls. and @pd> = "20080519"      | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/12/03<br>13:39 |
| S31 | 81     | 714/30.ccls. and @pd> = "20080519"     | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/12/03<br>14:55 |
| S32 | 77     | 714/42.ccls. and @pd> = "20080519"     | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/12/03<br>15:13 |
| S33 | 24     | 714/764.ccls. and<br>@pd> = "20080519" | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/12/03<br>15:16 |
| S34 | 22     | 714/773.ccls. and<br>@pd> = "20080519" | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2008/12/03<br>15:17 |
| S35 | 159298 | (memory or storage) with (correct\$4)  | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/12/04<br>08:48 |
| S36 | 37     | S35 and "functionally independent"     | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/12/04<br>08:48 |
| S37 | 119    | 714/5.ccls. and @pd> = "20081203"      | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/05/22<br>08:37 |
| S38 | 154    | 714/6.ccls. and @pd> = "20081203"      | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/05/22<br>08:43 |
| S39 | 64     | 714/30.ccls. and @pd> = "20081203"     | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/05/22<br>08:50 |

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| S40 | 68   | 714/42.ccls. and @pd> = "20081203"   | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/05/22<br>08:54 |
| S41 | 20   | 714/764.ccls. and<br>@pd> = "20081203"   | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/05/22<br>08:56 |
| S42 | 16   | 714/773.ccls. and<br>@pd> = "20081203"   | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON | 2009/05/22<br>08:56 |
| S43 | 274  | (MICHELEONI -RINO RAVASIO-<br>ROBERTO).in.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>08:58 |
| S44 | 2319 | 714/25.ccls.   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>09:11 |
| S45 | 19   | 714/25.ccls. and ("solid-state<br>memory" or "solid state memory")   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>09:12 |
| S46 | 95   | ("solid-state memory" or "solid state<br>memory") with correct\$3  | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>09:19 |
| S47 | 702  | ("solid-state memory" or "solid state<br>memory") and (correct\$3 with circuit)                              | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>09:57 |
| S48 | 41   | ("solid-state memory" or "solid state<br>memory") and ((correct\$3 with<br>circuit) with (first and second)) | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>09:57 |
| S49 | 2046 | (memory) with ((correct\$3 with<br>circuit) with (first and second))   | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>12:35 |

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| S50 | 324 | (memory) with ((correct\$3 with circuit) with (first and second)) and (different with error) | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2009/05/22<br>12:36 |
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**11/ 24/ 09 3:18:35 PM****C:\ Documents and Settings\ jmanoskey\ My Documents\ East\ Workspaces\ 10805182.wsp**